

# G31T-M7 Rev:1.0

Page Title of Schematic :

**Schematics Version History Table :**

Circuit Ver.	PCB Ver.	Total Page	Modified Page(s)	Date

Title	Page	Title	Page
P01 Cover Sheet	1		
P02 System Block Diagram	2		
P03 P4 LGA775P Part A	3		
P04 P4 LGA775P Part B	4		
P05 P4 LGA775P Part C	5		
P06 P4 LGA775P Part D	6		
P07 P4 LGA775P Part E	7		
P08 - CLK 9LPRS437	8		
P09 - ATX Power & Front Panel	9		
P10 - Vcore DC-DC RTL8802	10		
P11 - MIS DC-DC	11		
P12 - G31(MCH) HOST	12		
P13 - G31(MCH) PCIE/DMI/VGA	13		
P14 - G31(MCH) MEMORY	14		
P15 - G31(MCH) POWER	15		
P16 - DIMM1/2 (DDR2 SDRAM)	16		
P17 - PCIEX16 Slot	17		
P18 - ICH7 DMI/PCIE/USB/SATA	18		
P19 - ICH7 PCI/SPI/OTHER	19		
P20 - ICH7 POWER	20		
P21 - IDE / PCIEX1	21		
P22 - USB/SPI/Rear IO	22		
P23 - PCI Slot 1&2	23		
P24 - LPC_FDD/KB/M	24		
P25 - I/O Ports	25		
P26 - PCIE LAN L2-48	26		
P27 - AUDIO VT1708B(CHIP)	27		
P28 - AUDIO VT1708B(PANEL)	28		
P29 - Power Delivery Chart	29		
P30 - Power Sequence	30		

DEVICE	IDSEL	INT#	REQ#	GNT#
PCI1	17	C/D/E/F	PREQ-0	PGNT-0
PCI2	18	D/E/F/G	PREQ-1	PGNT-1

**VCORE POWER**  
PWM VRD11  
(RT8802A)

**INTEL  
P4 Processor**  
Core 2 Duo & Wolfdale  
LGA 775 pin

CLOCK GEN:9LPRS437

PCB : 244 x 210 mm ; 4 layers

FSB : 1066MHz & Freq : 266MHz  
FSB : 800MHz & Freq : 200MHz

PCIEx16

**INTEL  
G31**  
1210pin FC-BGA

BW : 10.7GB/s @ DDR2 :800/667MHz

DIMM1: DDR2 Socket 240P

VGA

DIMM2 : DDR2 Socket 240P

Analog Display  
RAMDAC: 400MHz  
Resolutions Up To 2048x1536@75Hz

USB V2.0

BW : 2GB/s

USB1  
2 ports

USB2  
2 ports

USB3  
2 ports

Up to Ultra ATA/100

**INTEL  
ICH7**  
652pin EBGA

PCIEx1  
BW : 133MB/s @ Freq : 33MHz

PCI1 Slot 120pin @ AD17

PCI2 Slot 120pin @ AD18

IDE1 40pin

One IDE Channel

Line in  
Line out  
Mic in

**Audio Codec  
VT1708B**

Azalia I/F

PCIE-LAN  
PCIE LAN L1e/L2-48

USBLAN  
RJ45

Flash Bios

SPI

LPC bus

**Super I/O  
IT8713F**  
128pin PQFP

SATA1 7Pin

SATA2 7pin

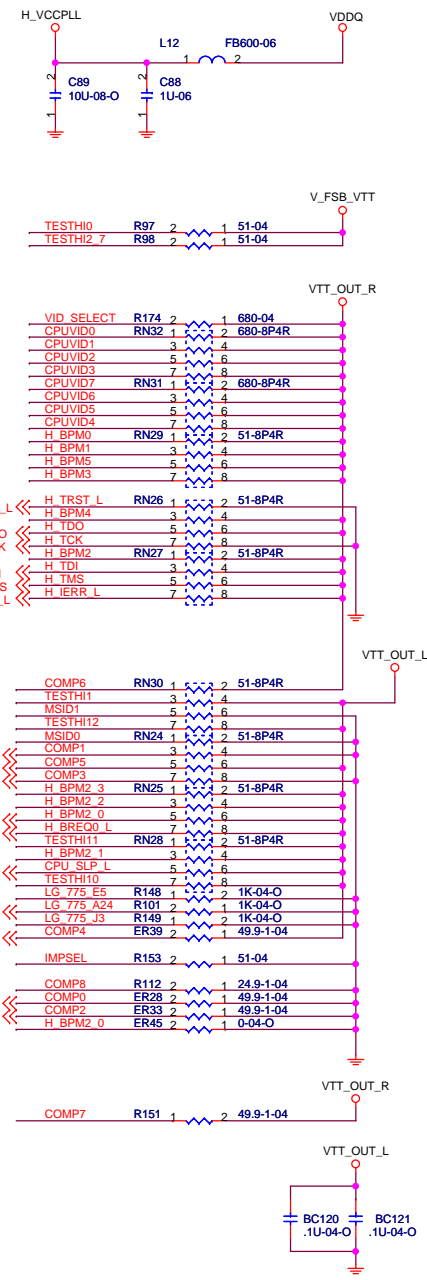
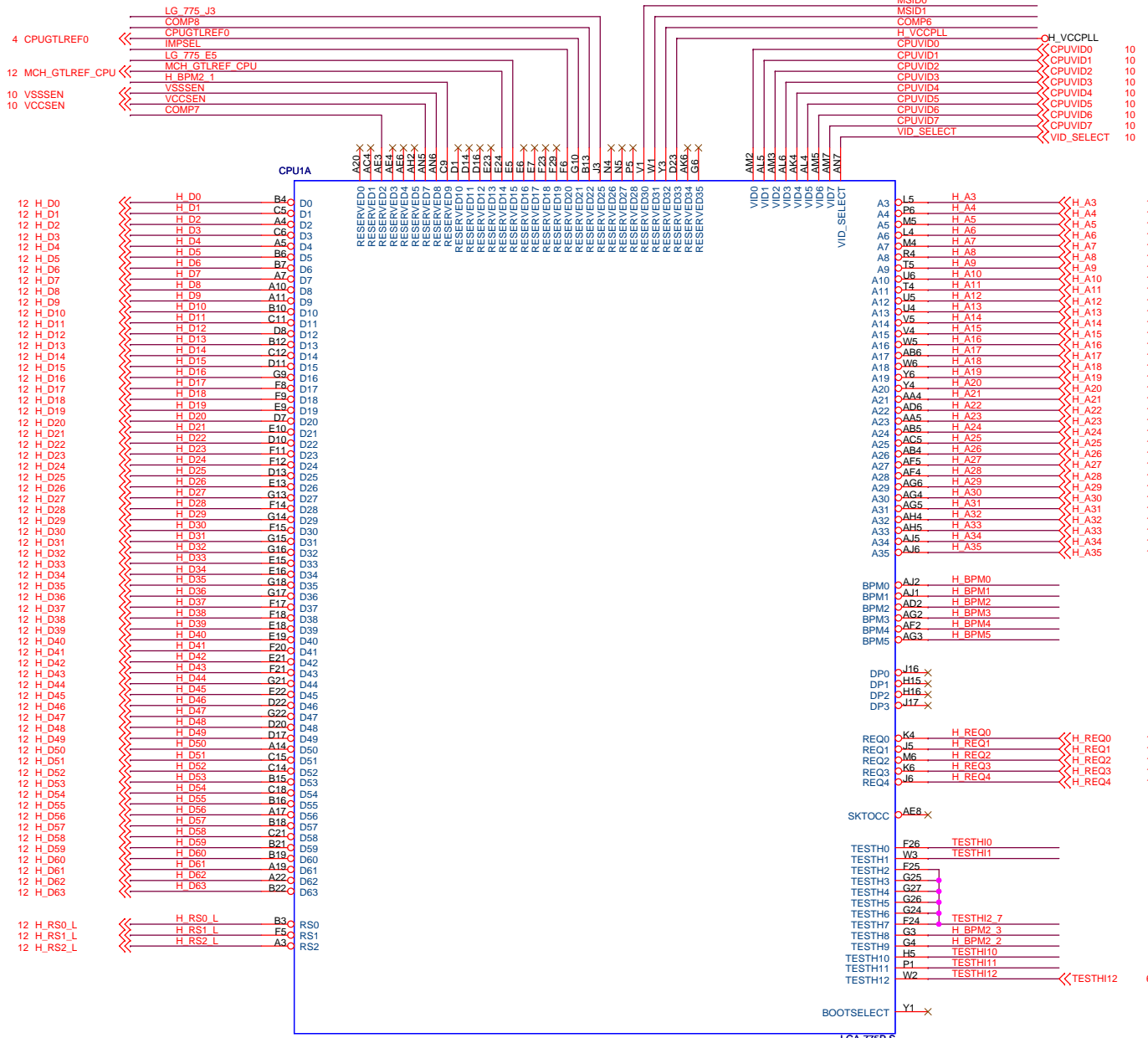
SATA3 7Pin

SATA4 7pin

BW : 150MB/s

[www.schematic-x.blogspot.com](http://www.schematic-x.blogspot.com)

<b>ECS Elitegroup Computer Systems</b>			
Title <b>System Block Diagram</b>			
Size B	Document Number <b>G31T-M7</b>		Rev <b>1.0</b>
Date:	Wednesday, May 28, 2008	Sheet 2	of 30

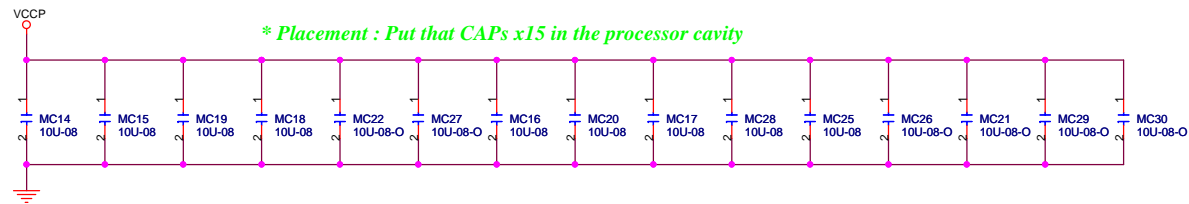




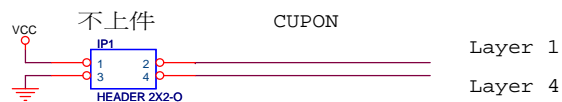
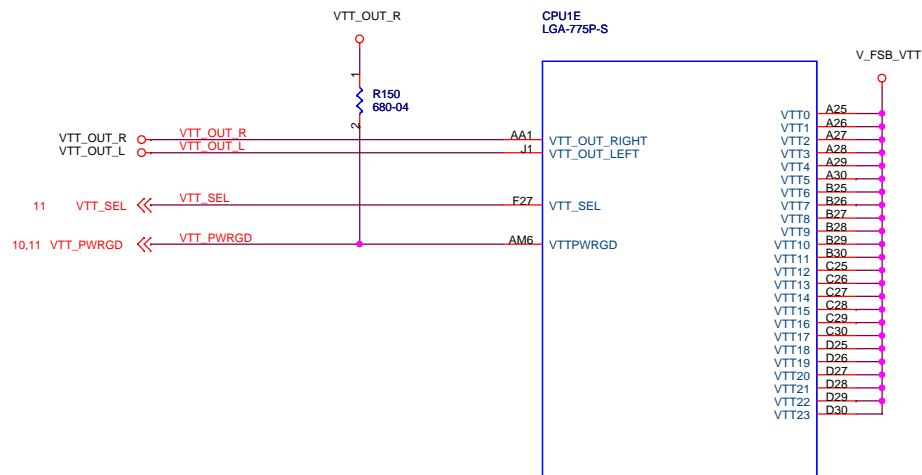
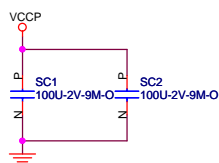


CPU1C  
LGA-775P-S

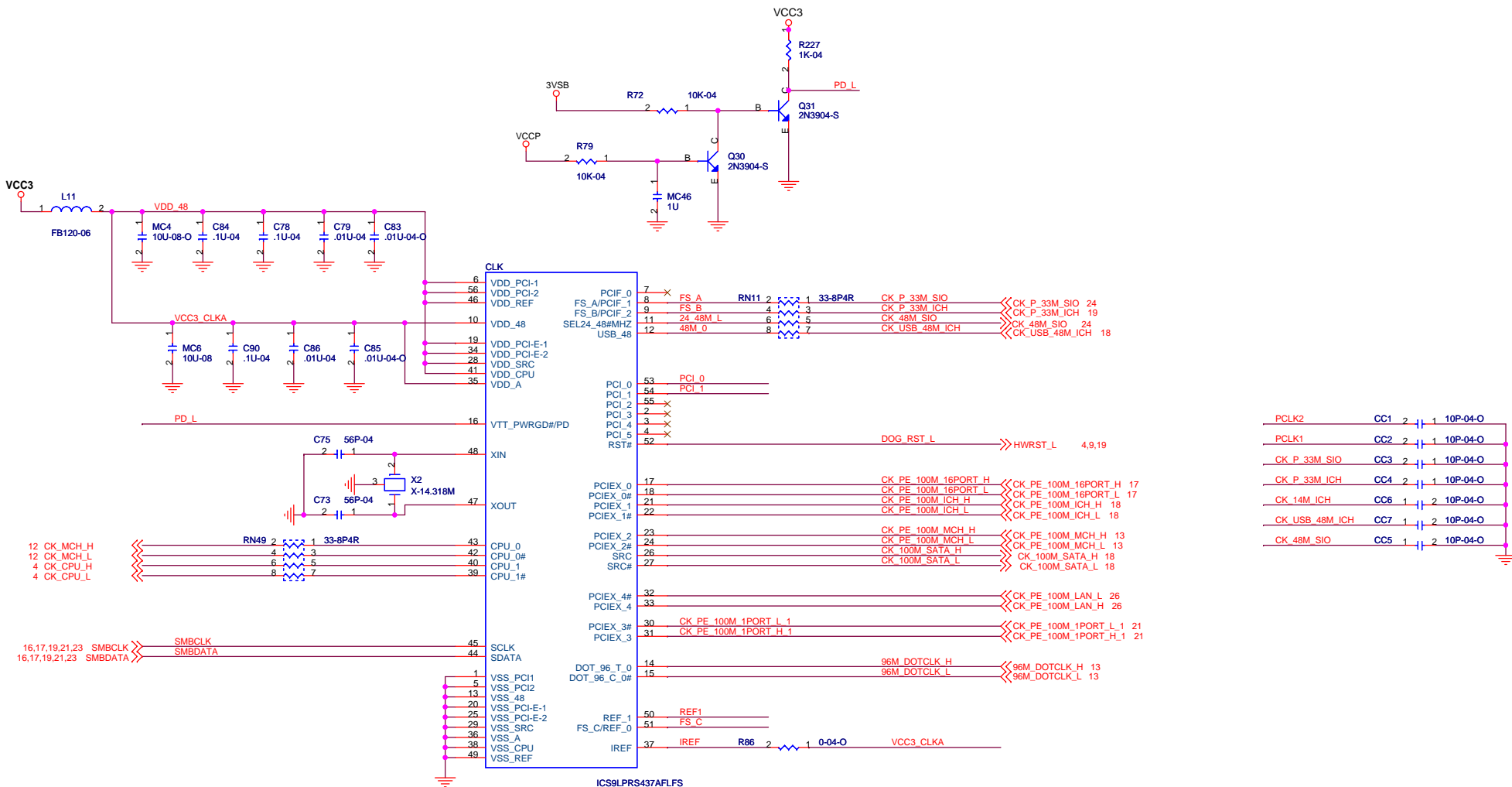




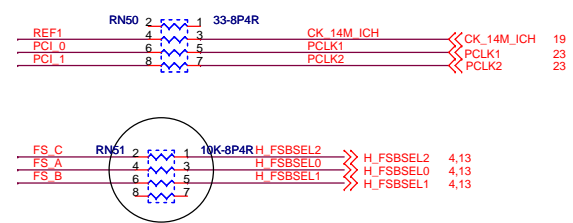
\* Placement : Put that CAPs x2 on solder side



1080 : trace width 4 mil 50 ohm  
Trace Length 3150 mils  
Spacing: 1.clearance to itself 50/4/50(S:W:S)  
2.clearance to other signal 3W

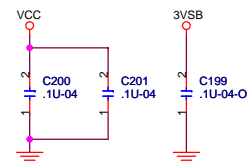
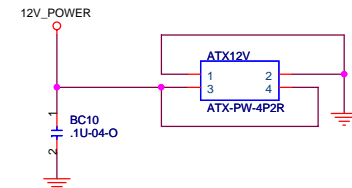
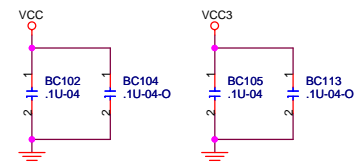
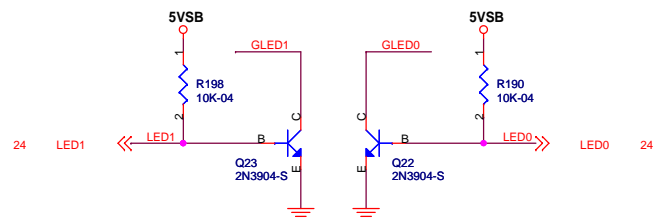
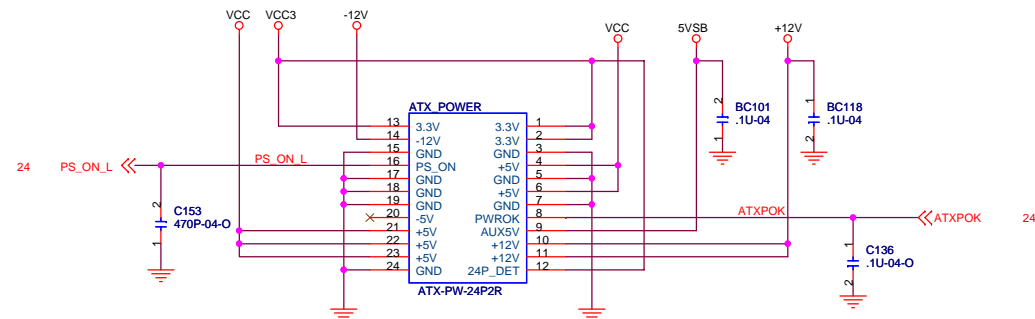
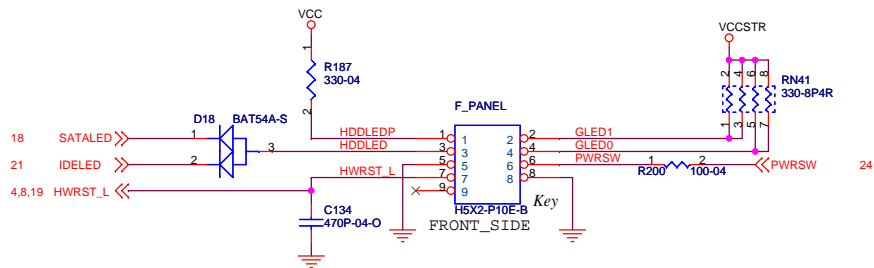


BSEL TABLE	
2 1 0 PSB FREQUENCY	
0 0 1	133 MHZ (533)
0 1 0	200 MHZ (800)
0 0 0	266 MHZ (1066)

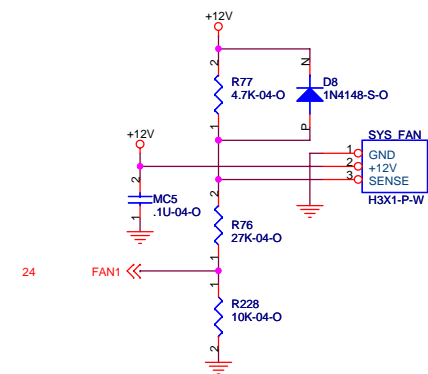
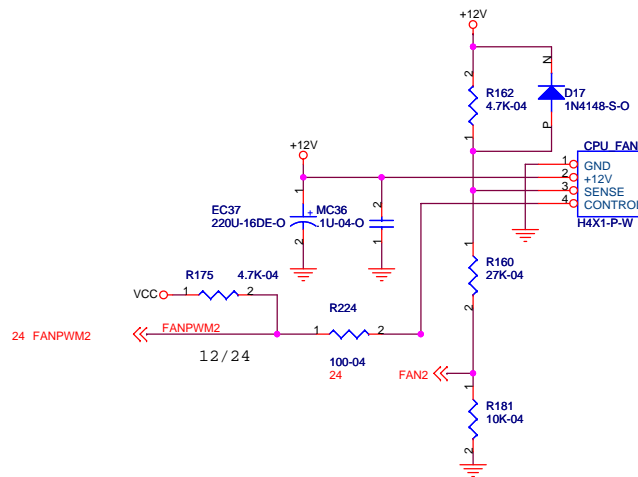
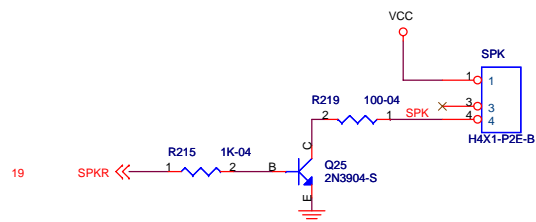


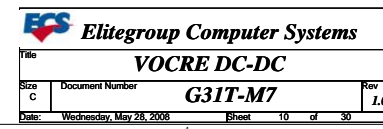
Swap PCIEX16 and SATA clock  
Change ICH clk to pin 8

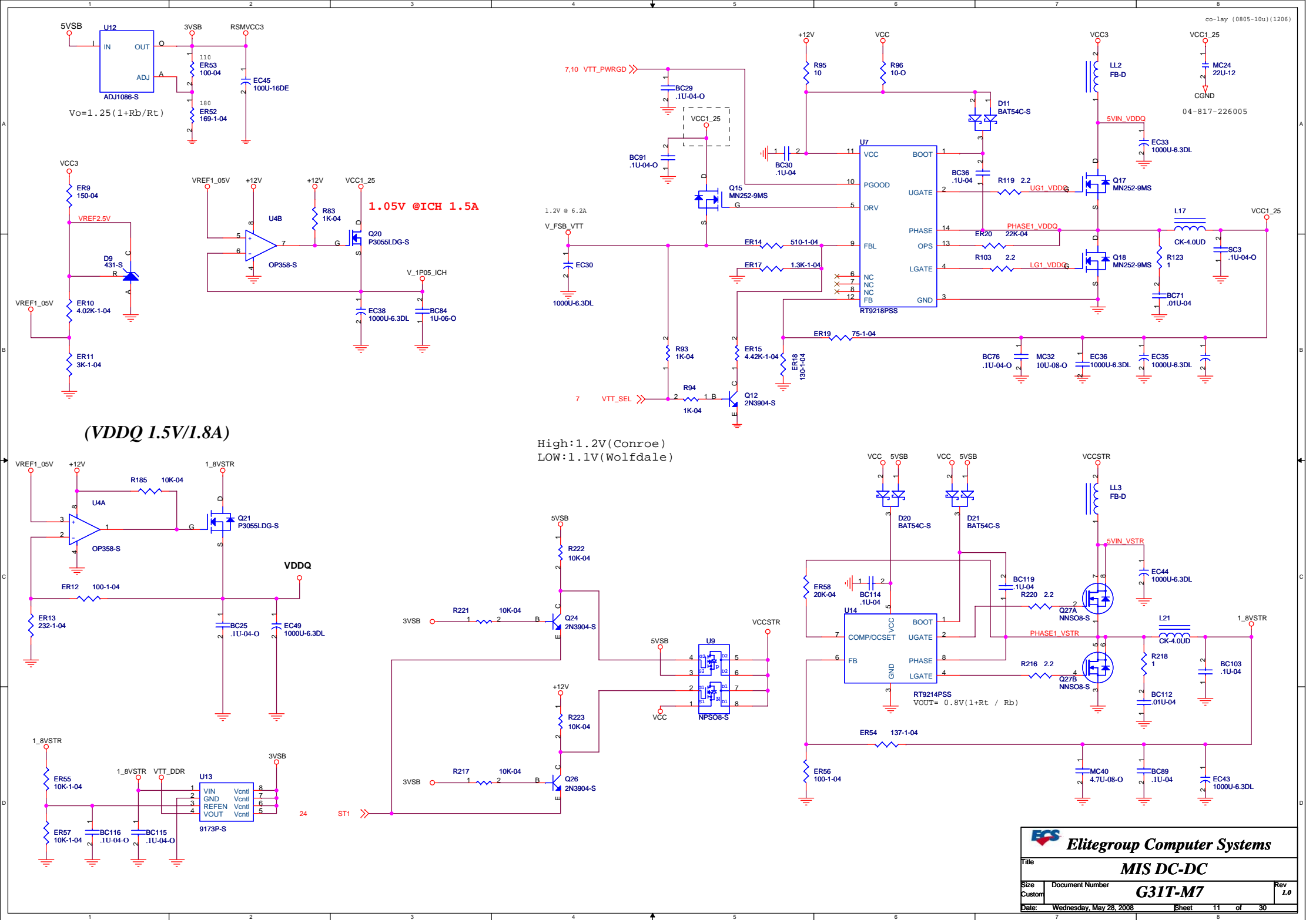


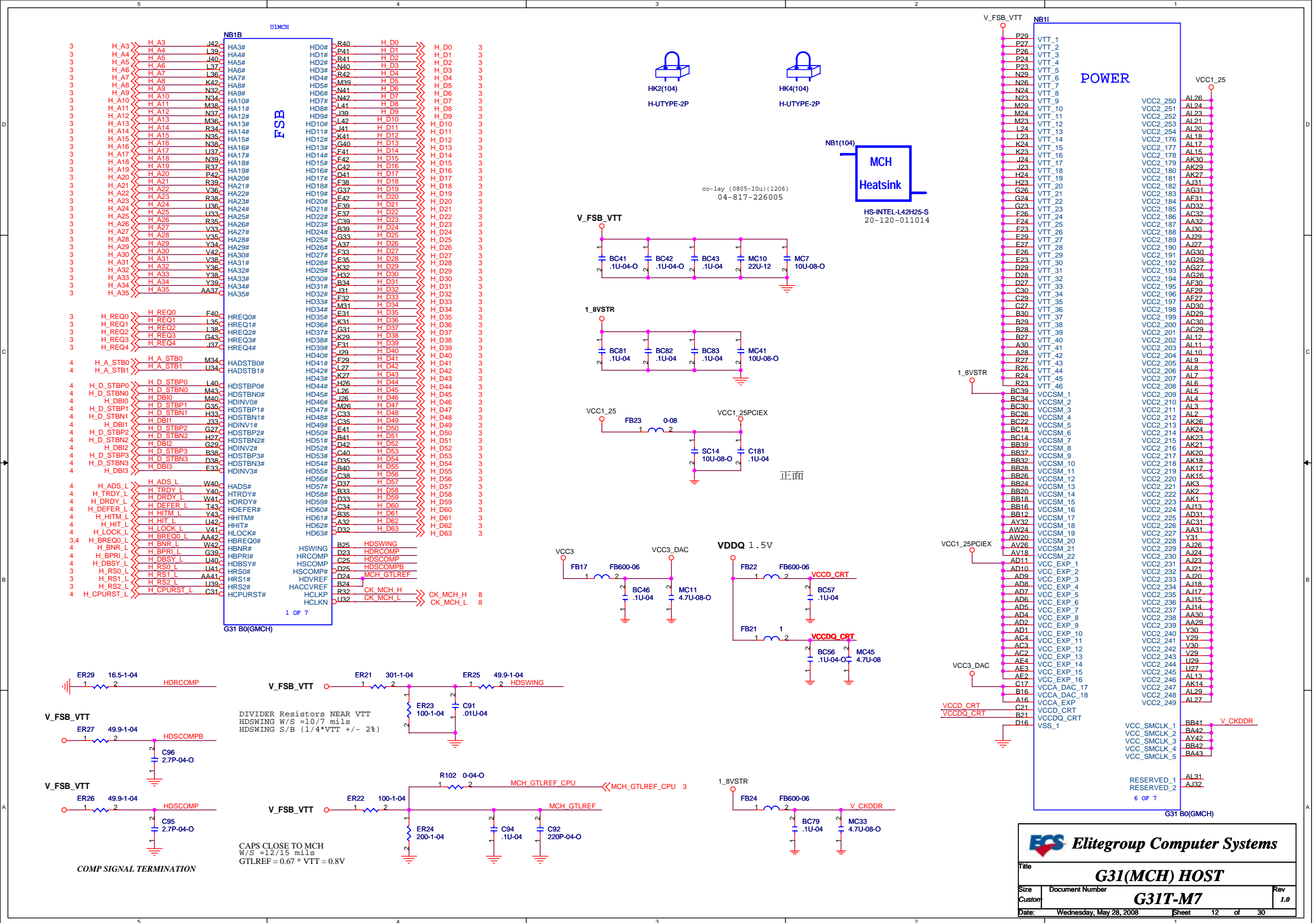


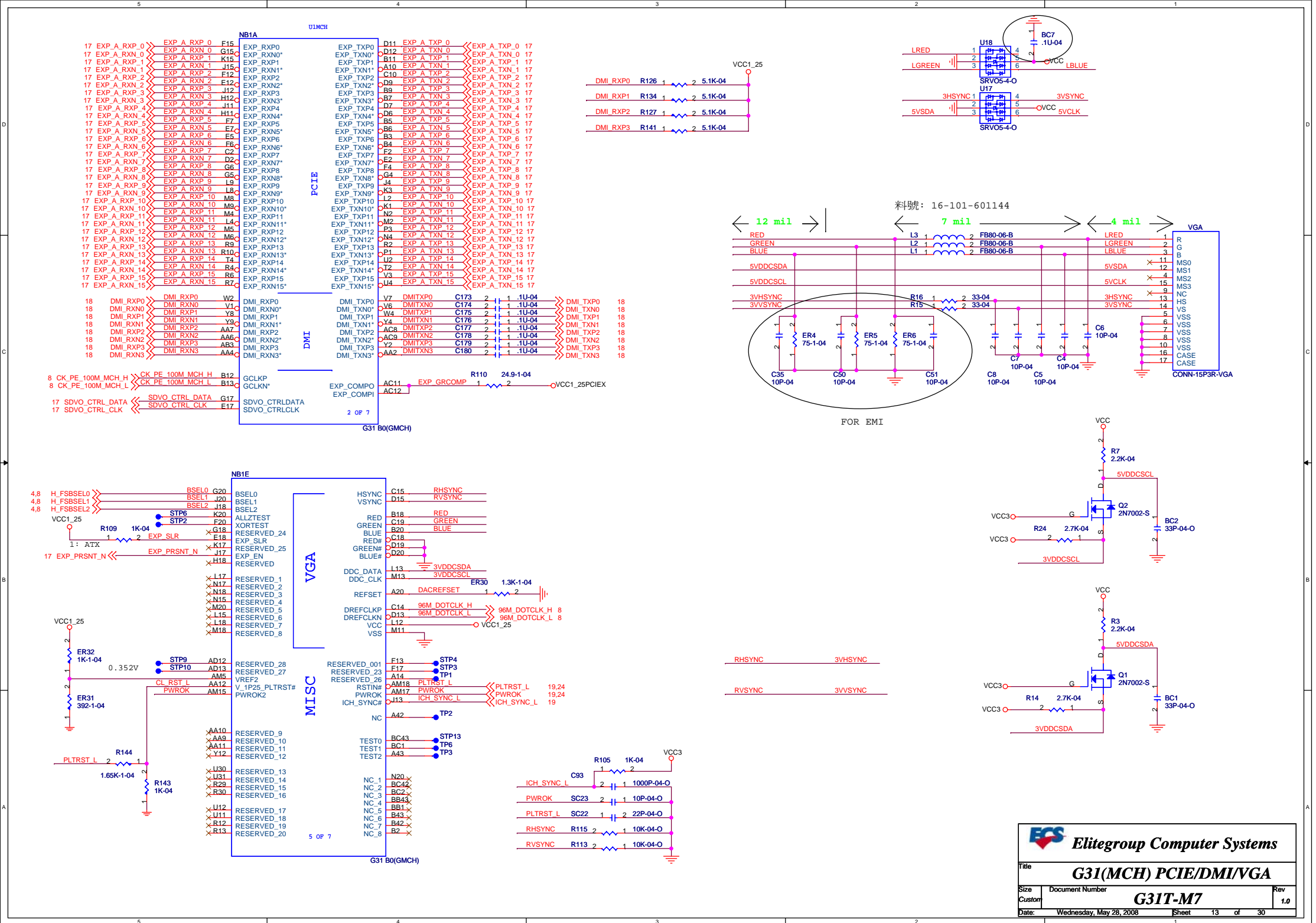
	AC_ON	S0	S1	S3	S5
LED0 (PIN78)	0	1	B	B	0
LED1 (PIN84)	0	0	0	1	0
-GLED0 (PIN4)	0	0	B	B	0
-GLED1 (PIN2)	0	1	1	0	0
LED STATE	OFF	Green	G-blink	Y-blink	OFF

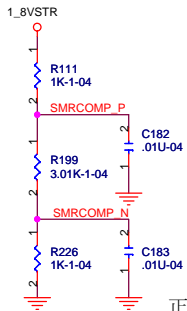
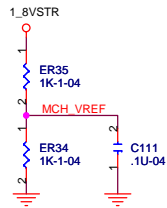








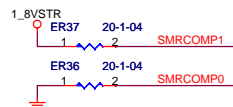
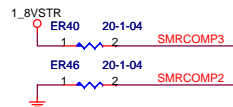




正面

#### DESIGN NOTE:

BUFFERS CALIBRATE TO  
20/80% OF V<sub>SM</sub> INTERNAL  
BUFFERS SET TO 20 OHMS



NB1C		U1MCH	
MAAA_0	BA31	SMA_A0	SDQS_A0
MAAA_1	BB25	SMA_A1	SDQS_A0#
MAAA_2	BA25	SMA_A2	SDM_A0
MAAA_3	BA25	SMA_A3	
MAAA_4	AY25	SMA_A4	SDQ_A0
MAAA_5	BA23	SMA_A5	SDQ_A1
MAAA_6	AY24	SMA_A6	SDQ_A2
MAAA_7	AY23	SMA_A7	SDQ_A3
MAAA_8	BB23	SMA_A8	SDQ_A4
MAAA_9	BA22	SMA_A9	SDQ_A5
MAAA_10	AY33	SMA_A10	SDQ_A6
MAAA_11	BB22	SMA_A11	SDQ_A7
MAAA_12	AW21	SMA_A12	
MAAA_13	AY38	SMA_A13	SDQS_A1
MAAA_14	BA21	SMA_A14	SDQS_A1#
			SDM_A1
SWE_L_A	BB34	SWE_A#	SDQ_A8
SCAS_L_A	AY35	SCAS_A#	SDQ_A9
SRAS_L_A	BB33	SRAS_A#	SDQ_A10
SBSA_0	BA33	SBS_A0	SDQ_A11
SBSA_1	AW32	SBS_A1	SDQ_A12
SBSA_2	BB21	SBS_A2	SDQ_A13
			SDQ_A14
CSA_L0	AW35	SCS_A0#	SDQ_A15
CSA_L1	BA35	SCS_A1#	
	BB38	RESERVED_11	SDQS_A2
		RESERVED_12	SDQS_A2#
			SDM_A2
CKEA_0	BC20	SCKE_A0	SDQ_A16
CKEA_1	AY20	SCKE_A1	SDQ_A17
	AY21	RESERVED_9	SDQ_A18
	BA19	RESERVED_10	SDQ_A19
ODTA_0	AY37	SODT_A0	SDQ_A20
ODTA_1	BA38	SODT_A1	SDQ_A21
	BB35	RESERVED_2	SDQ_A22
	BA39	RESERVED_1	SDQ_A23
DCLKA_H0	AU31	SCLK_A0	SDQS_A3
DCLKA_L0	AR31	SCLK_A0#	SDQS_A3#
DCLKA_H1	AP27	SCLK_A1	SDM_A3
DCLKA_L1	AN27	SCLK_A1#	
DCLKA_H2	AW33	SCLK_A2	SDQ_A24
DCLKA_L2	AW33	SCLK_A2#	SDQ_A25
	AP29	RESERVED_3	SDQ_A26
	AP31	RESERVED_4	SDQ_A27
	AM26	RESERVED_5	SDQ_A28
	AM27	RESERVED_6	SDQ_A29
	AT33	RESERVED_7	SDQ_A30
	AU33	RESERVED_8	SDQ_A31
			SDQS_A4
			SDQS_A4#
			SDM_A4
			SDQ_A32
			SDQ_A33
			SDQ_A34
			SDQ_A35
			SDQ_A36
			SDQ_A37
			SDQ_A38
			SDQ_A39
			SDQS_A5
			SDQS_A5#
			SDM_A5
			SDQ_A40
			SDQ_A41
			SDQ_A42
			SDQ_A43
			SDQ_A44
			SDQ_A45
			SDQ_A46
			SDQ_A47
			SDQS_A6
			SDQS_A6#
			SDM_A6
			SDQ_A48
			SDQ_A49
			SDQ_A50
			SDQ_A51
			SDQ_A52
			SDQ_A53
			SDQ_A54
			SDQ_A55
			SDQS_A7
			SDQS_A7#
			SDM_A7
			SDQ_A56
			SDQ_A57
			SDQ_A58
			SDQ_A59
			SDQ_A60
			SDQ_A61
			SDQ_A62
			SDQ_A63

DDR\_0

AN21

3 OF 7

G31 B0(GMCH)

NB1D		U1MCH	
MAAB_0	BB17	SMA_B0	SDQS_B0
MAAB_1	AY17	SMA_B1	SDQS_B0#
MAAB_2	BA17	SMA_B2	SDM_B0
MAAB_3	BC16	SMA_B3	
MAAB_4	AW15	SMA_B4	SDQ_B0
MAAB_5	BA15	SMA_B5	SDQ_B1
MAAB_6	BB15	SMA_B6	SDQ_B2
MAAB_7	BA14	SMA_B7	SDQ_B3
MAAB_8	BA14	SMA_B8	SDQ_B4
MAAB_9	BB14	SMA_B9	SDQ_B5
MAAB_10	AW18	SMA_B10	SDQ_B6
MAAB_11	BB13	SMA_B11	SDQ_B7
MAAB_12	BA13	SMA_B12	
MAAB_13	AY29	SMA_B13	SDQS_B1
MAAB_14	AY13	SMA_B14	SDQS_B1#
			SDM_B1
SWE_L_B	BA27	SWE_B#	SDQ_B8
SCAS_L_B	AW29	SCAS_B#	SDQ_B9
SRAS_L_B	AW26	SRAS_B#	SDQ_B10
SBSB_0	AY19	SBS_B0	SDQ_B11
SBSB_1	BA18	SBS_B1	SDQ_B12
SBSB_2	BC12	SBS_B2	SDQ_B13
			SDQ_B14
CSB_L0	BB27	SCS_B0#	SDQ_B15
CSB_L1	BB30	SCS_B1#	
	AY27	RESERVED_13	SDQS_B2
	AY31	RESERVED_10	SDQS_B2#
			SDM_B2
CKEB_0	AY12	SCKE_B0	SDQ_B16
CKEB_1	AW12	SCKE_B1	SDQ_B17
	BB11	RESERVED_11	SDQ_B18
	BA29	RESERVED_12	SDQ_B19
ODTB_0	BA29	SODT_B0	SDQ_B20
ODTB_1	BA30	SODT_B1	SDQ_B21
	BB29	RESERVED_14	SDQ_B22
	BB31	RESERVED_15	SDQ_B23
DCLKB_H0	AV31	SCLK_B0	SDQS_B3
DCLKB_L0	AW31	SCLK_B0#	SDQS_B3#
DCLKB_H1	AU27	SCLK_B1	SDM_B3
DCLKB_L1	AT27	SCLK_B1#	
DCLKB_H2	AV32	SCLK_B2	SDQ_B24
DCLKB_L2	AT32	SCLK_B2#	SDQ_B25
	AU29	RESERVED_16	SDQ_B26
	AR29	RESERVED_17	SDQ_B27
	AV29	RESERVED_18	SDQ_B28
	AW27	RESERVED_19	SDQ_B29
	AN33	RESERVED_20	SDQ_B30
	AP32	RESERVED_21	SDQ_B31
			SDQS_B4
			SDQS_B4#
			SDM_B4
			SDQ_B32
			SDQ_B33
			SDQ_B34
			SDQ_B35
			SDQ_B36
			SDQ_B37
			SDQ_B38
			SDQ_B39
			SDQS_B5
			SDQS_B5#
			SDM_B5
			SDQ_B40
			SDQ_B41
			SDQ_B42
			SDQ_B43
			SDQ_B44
			SDQ_B45
			SDQ_B46
			SDQ_B47
			SDQS_B6
			SDQS_B6#
			SDM_B6
			SDQ_B48
			SDQ_B49
			SDQ_B50
			SDQ_B51
			SDQ_B52
			SDQ_B53
			SDQ_B54
			SDQ_B55
			SDQS_B7
			SDQS_B7#
			SDM_B7
			SDQ_B56
			SDQ_B57
			SDQ_B58
			SDQ_B59
			SDQ_B60
			SDQ_B61
			SDQ_B62
			SDQ_B63

DDR\_1

AM21

MCH\_VREF

SVREF

SMRCOMP0

SMRCOMP1

SMRCOMP2

SMRCOMP3

SMRCOMP N

SMRCOMPVOL

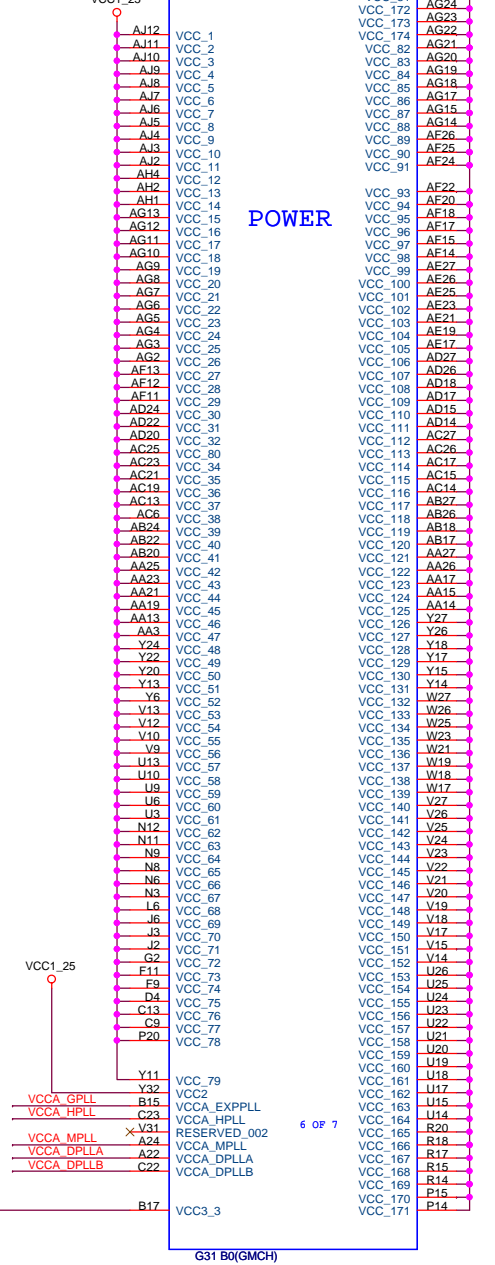
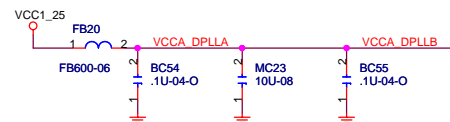
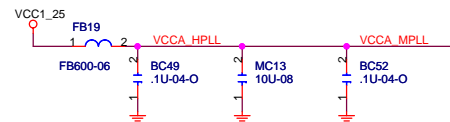
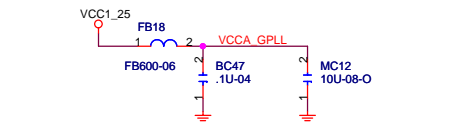
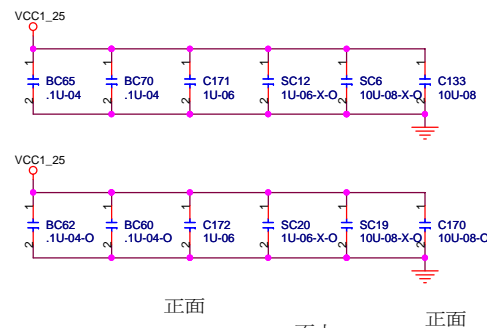
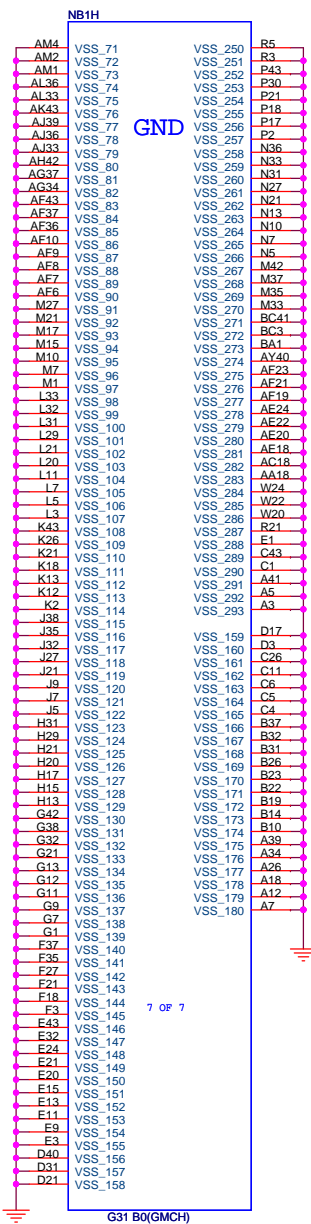
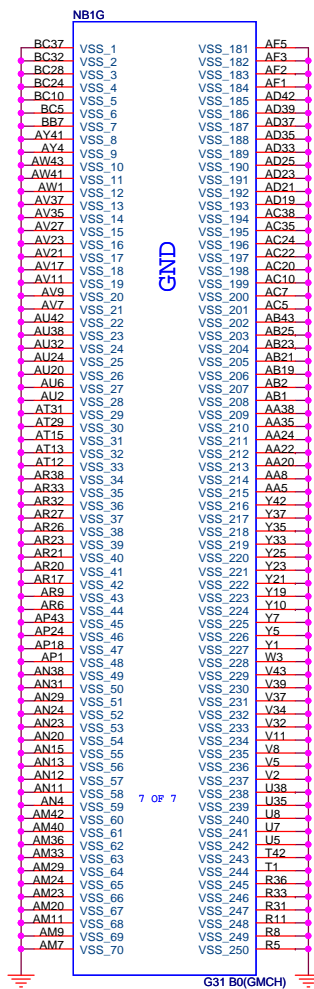
SMRCOMPVOH

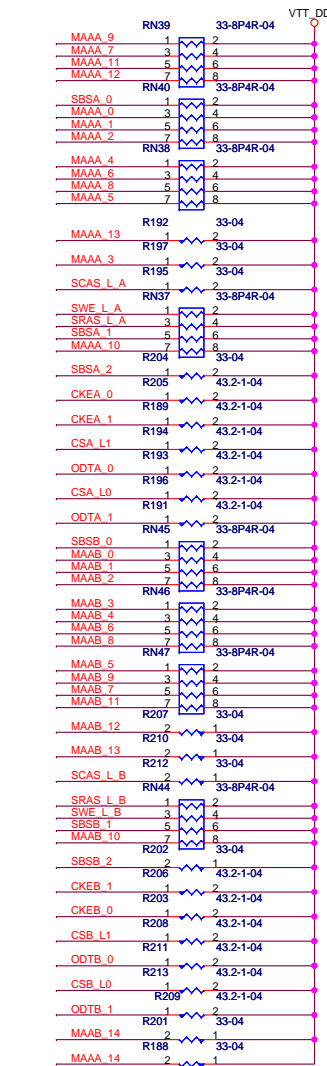
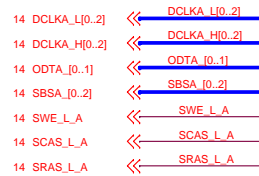
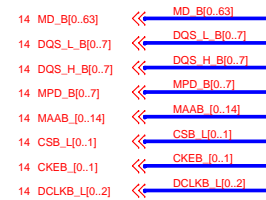
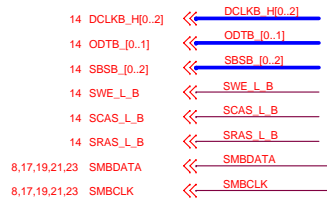
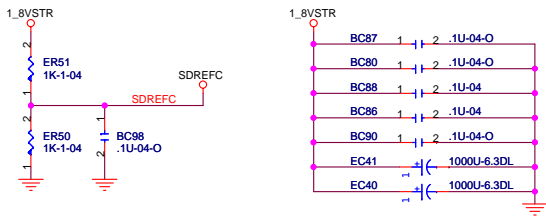
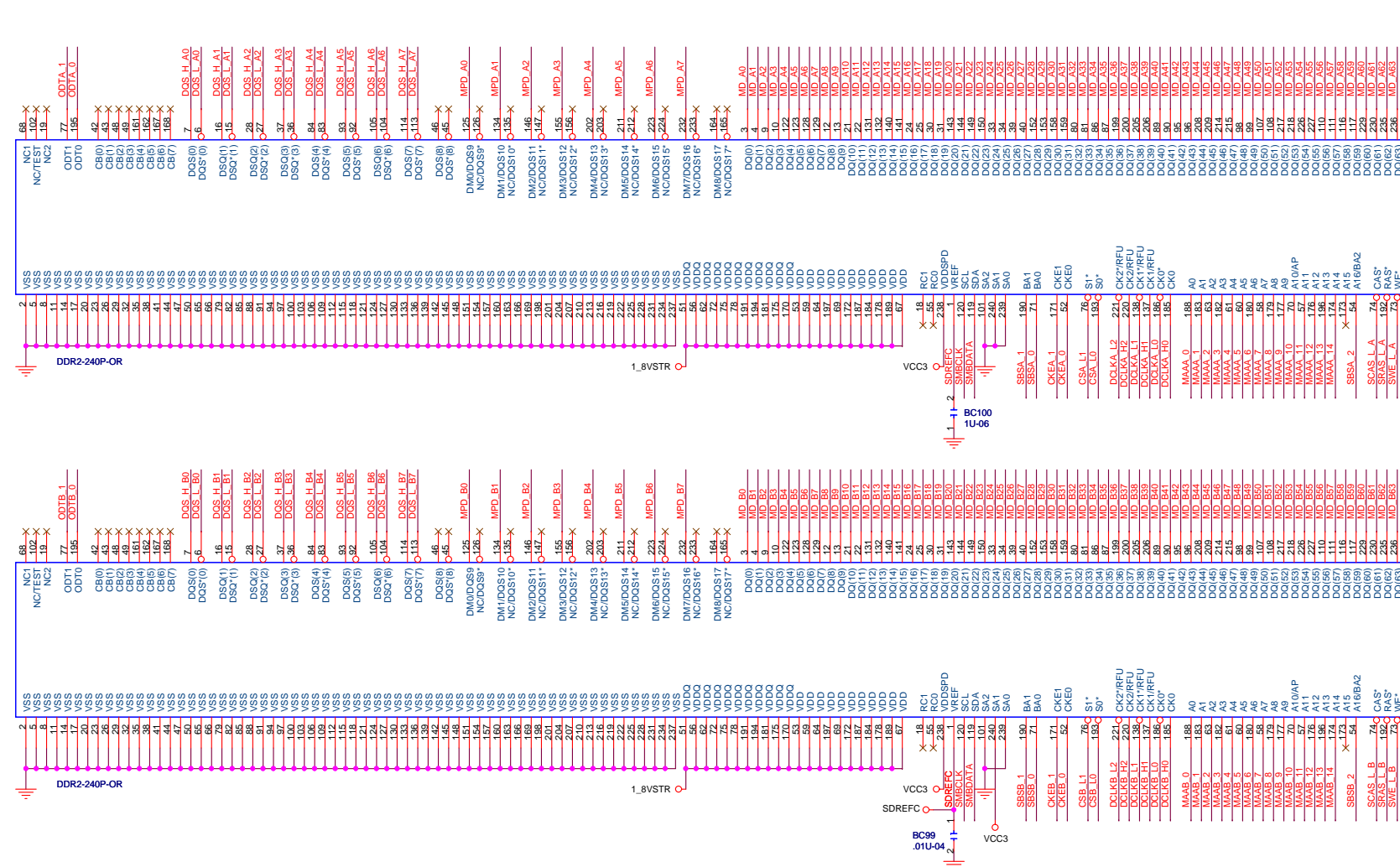
RESERVED

4 OF 7

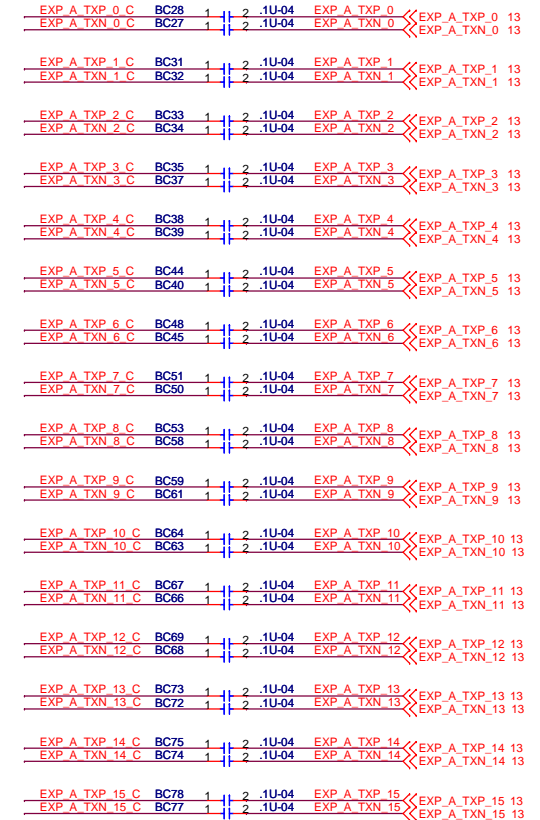
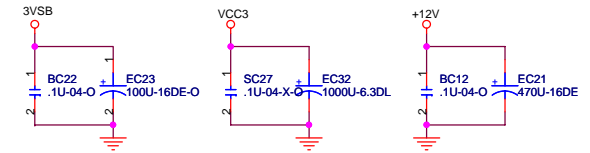
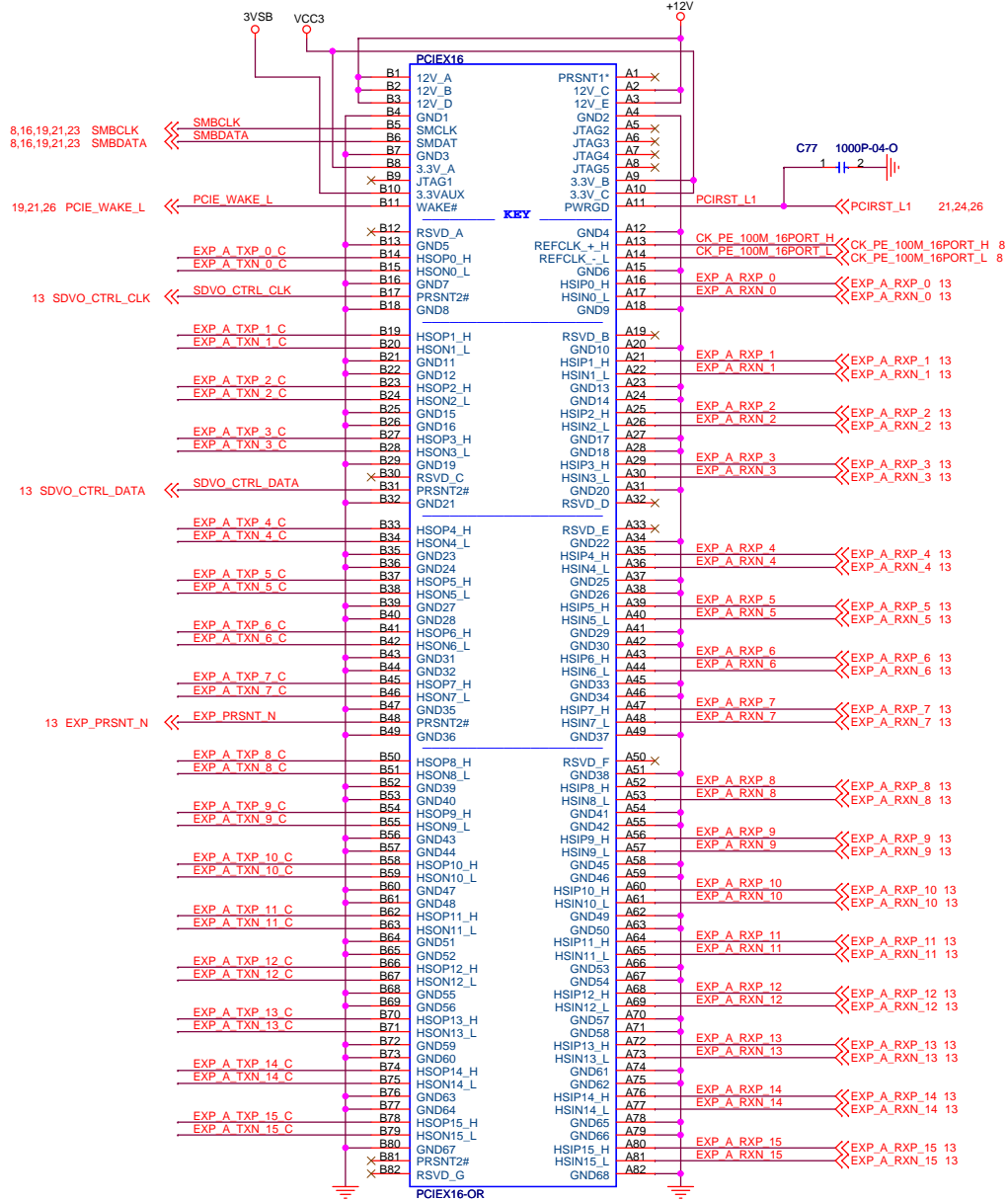
G31 B0(GMCH)



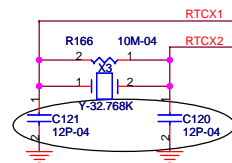
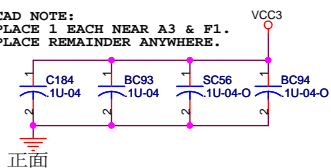
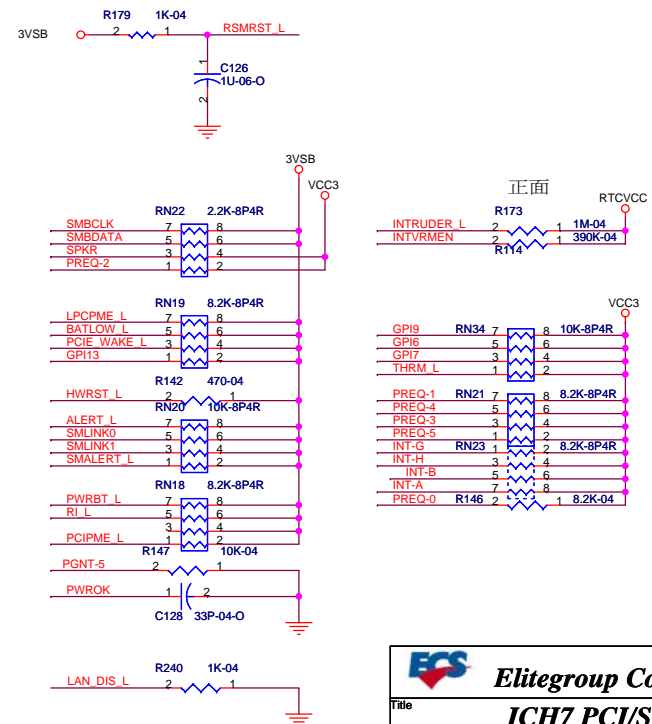
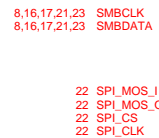




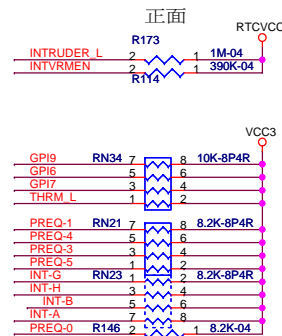




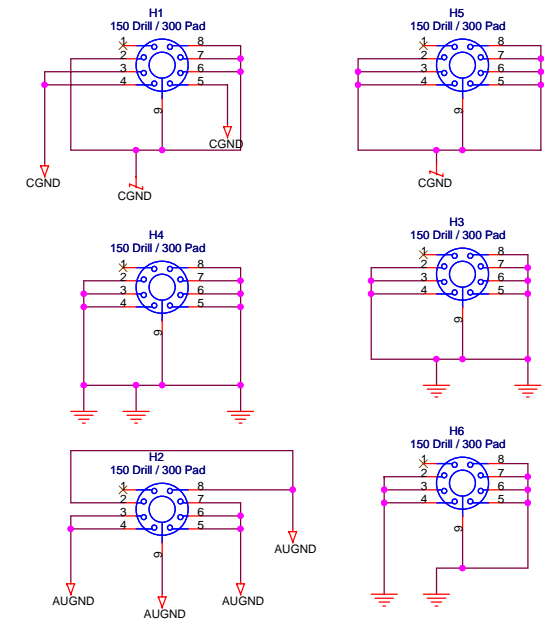
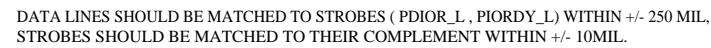


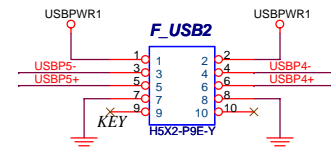
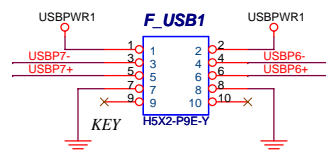
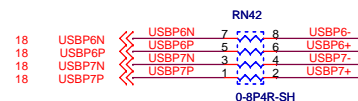
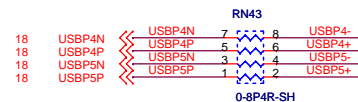
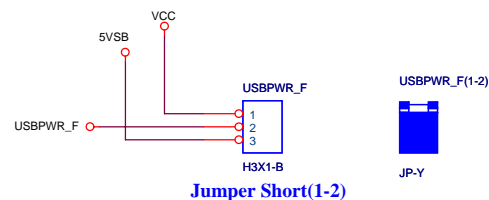
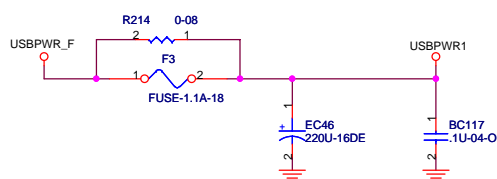
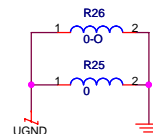
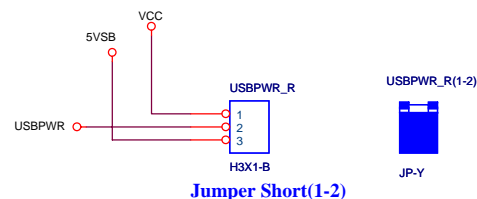
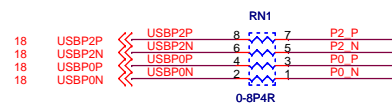


X3(wire)



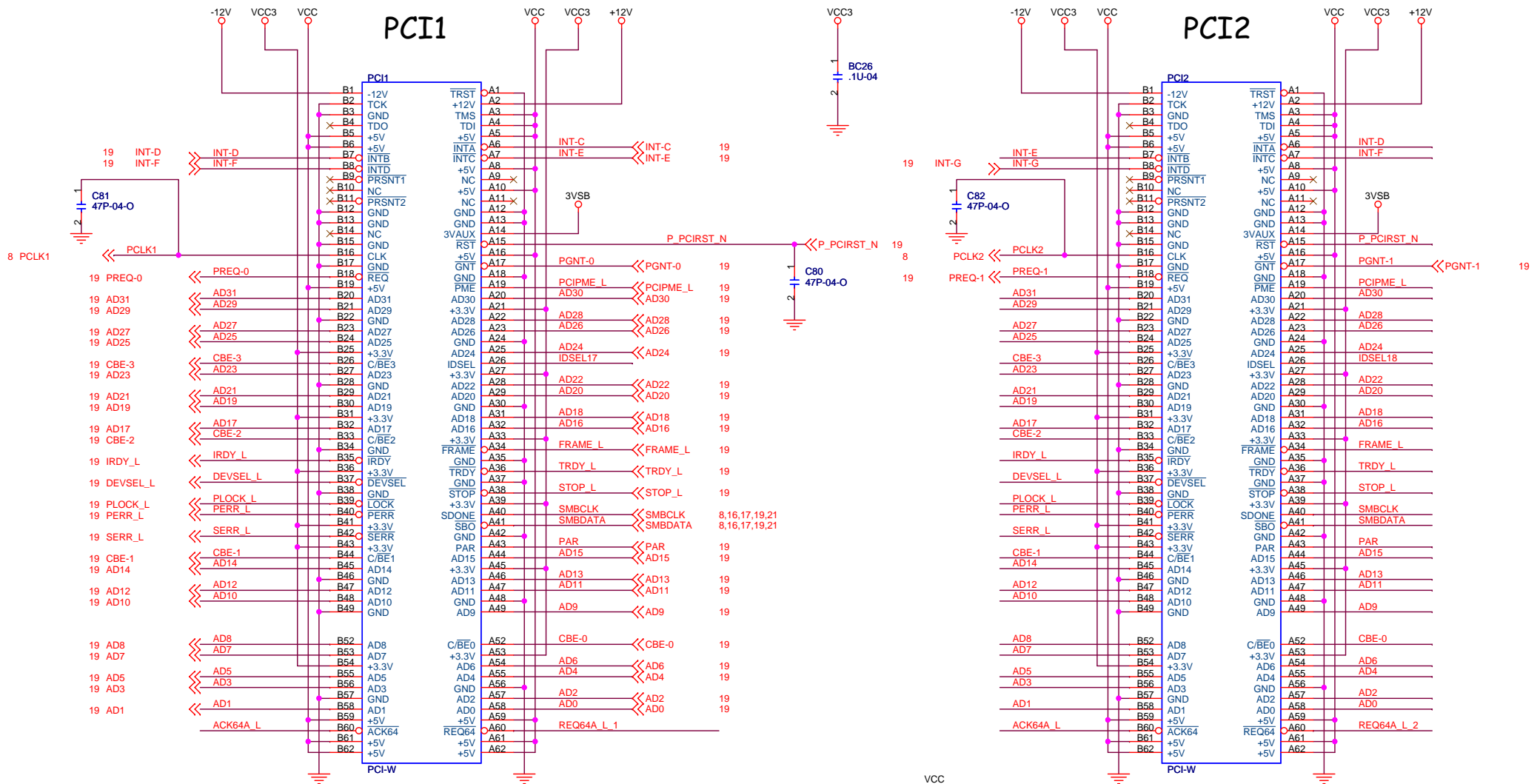






# PCI1

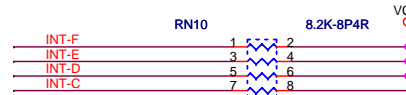
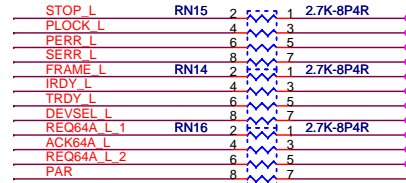
# PCI2



PCI1-INT:  
INTA:INTC  
INTB:INTD  
INTC:INTE  
INTD:INTF

IDSEL=AD17  
REQ=PREQ0#  
GNT=PGNT0#

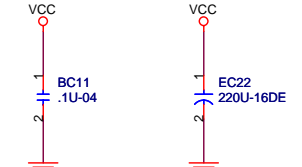
AD17 R99 300-04 IDSEL17



PCI2-INT:  
INTA:INTD  
INTB:INTE  
INTC:INTF  
INTD:INTG

IDSEL=AD18  
REQ=PREQ1#  
GNT=PGNT1#

AD18 R100 300-04 IDSEL18



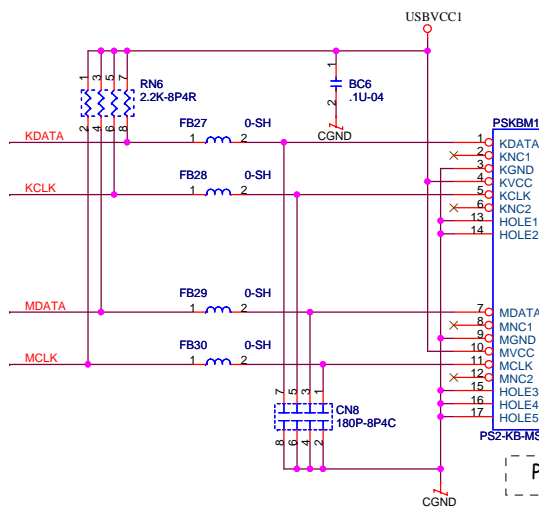
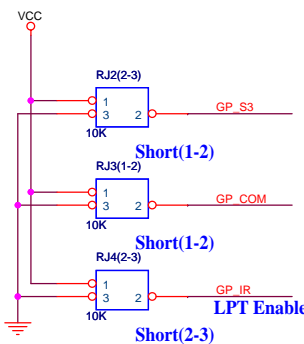
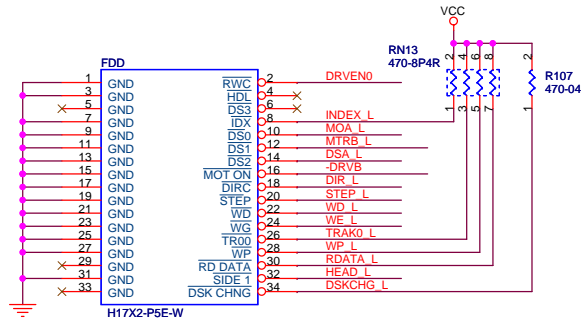
Elitegroup Computer Systems

Title  
**PCI Slot 1&2**

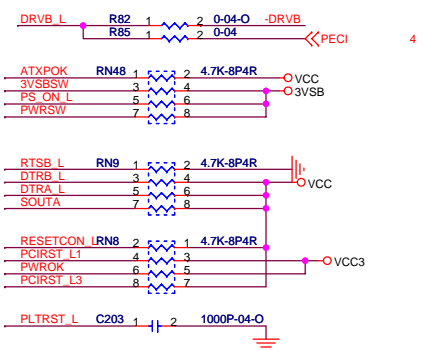
Size Custom Document Number **G31T-M7** Rev 1.0

Date: Wednesday, May 28, 2008 Sheet 23 of 30

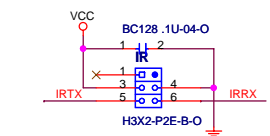
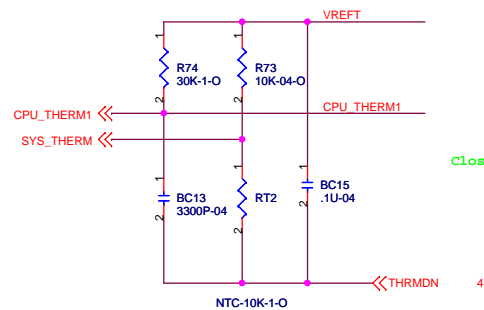




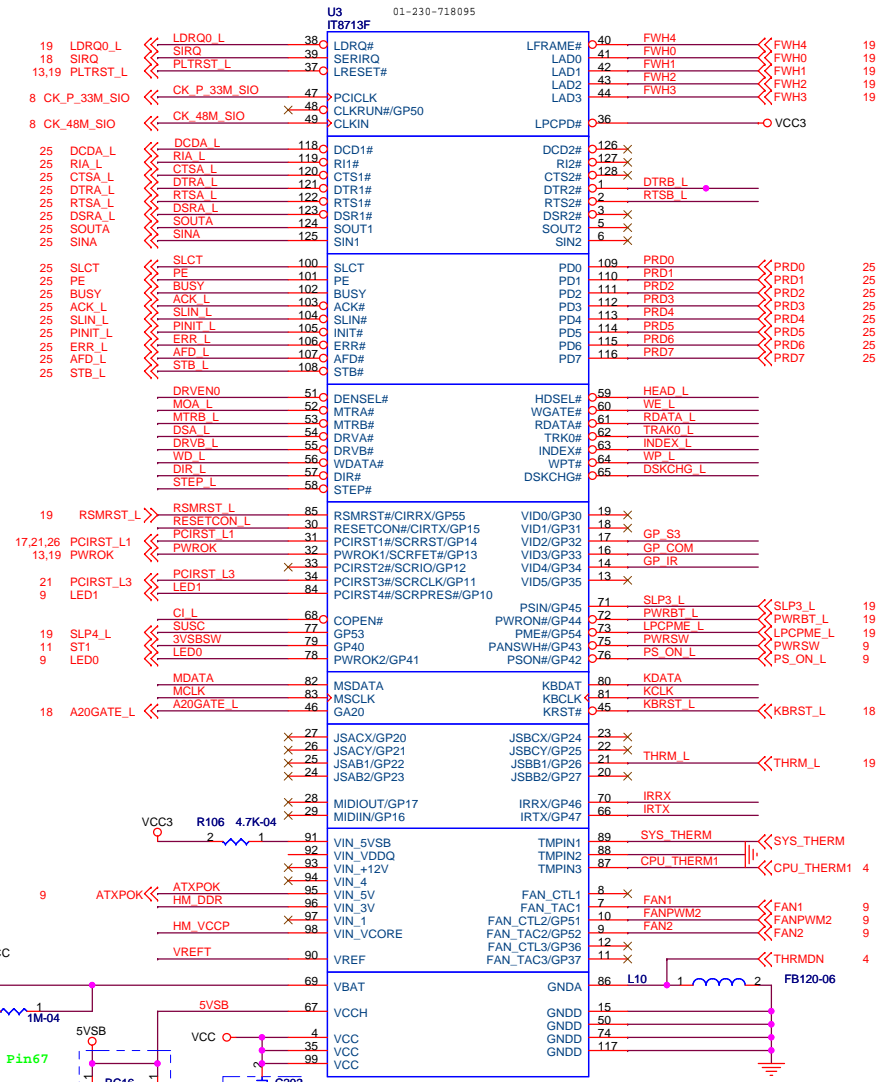
install PECI when 8718



### THERM. SENSING



修改U3 Value:8718F



Close to Place the Pin67

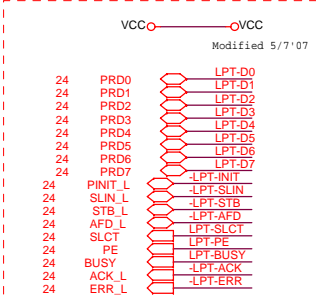
Close to Place the Pin99

IOGND have to 10mil width



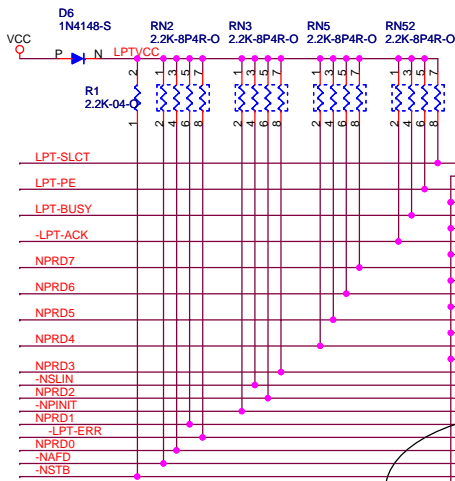
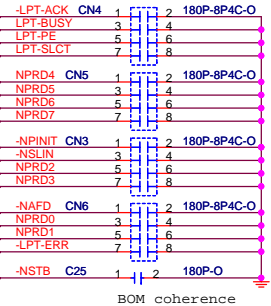
LPT

External Connection



If you found anything wrong with this circuit, please contact with Jack Hu (Ext:622)

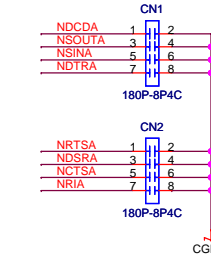
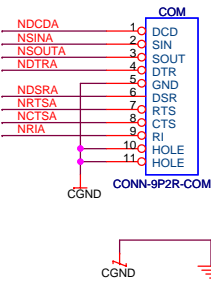
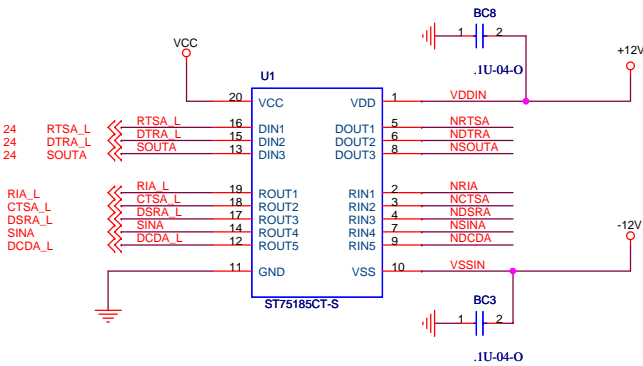
LPT-D4	NPRD4
LPT-D5	NPRD5
LPT-D6	NPRD6
LPT-D7	NPRD7
LPT-INIT	NPINIT
LPT-SLIN	NSLIN
LPT-D2	NPRD2
LPT-D3	NPRD3
LPT-STB	NSTB
LPT-AFD	NAFD
LPT-D0	NPRD0
LPT-D1	NPRD1



Selection design with LPT1

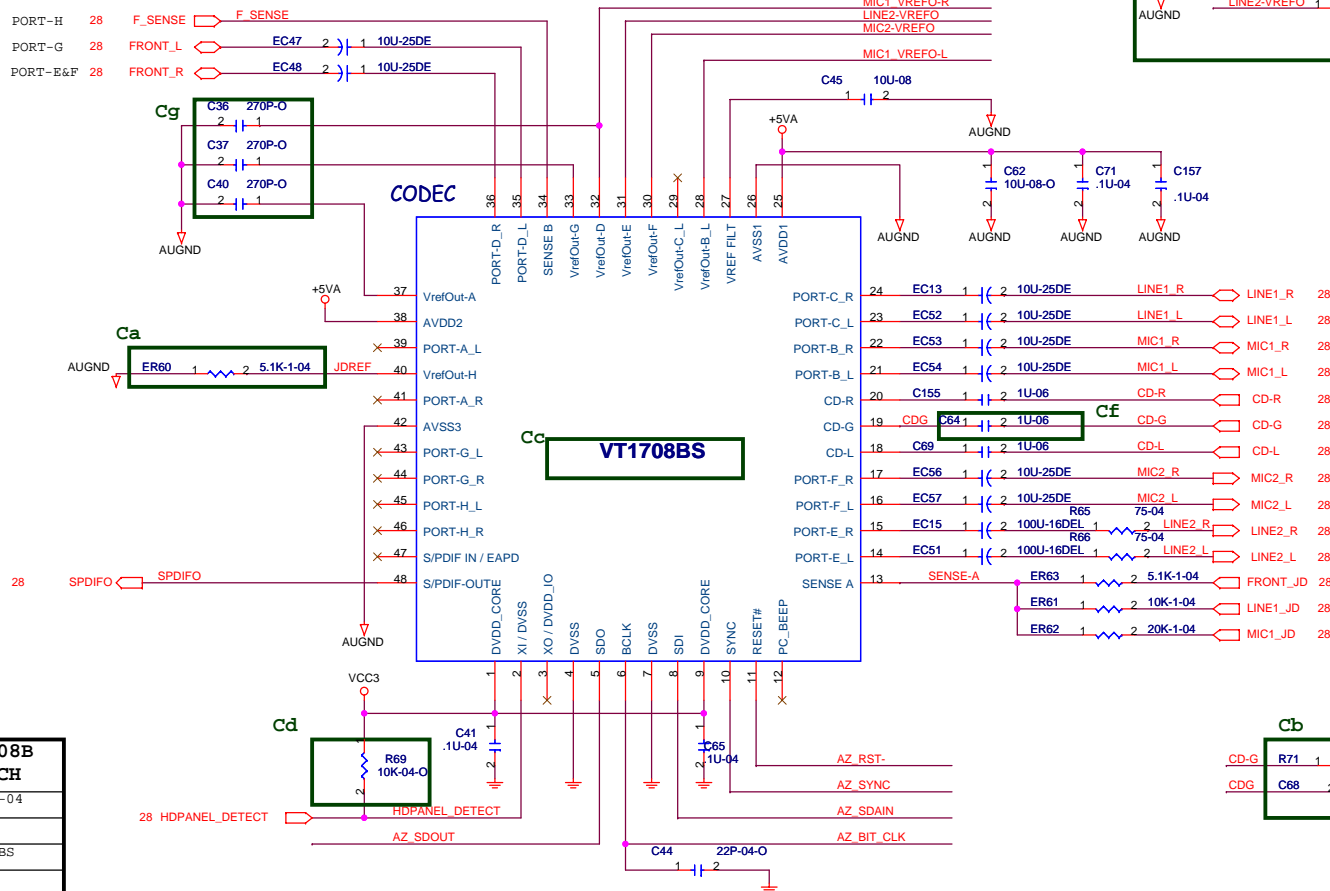
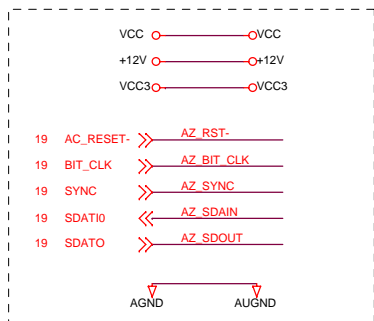
-NSTB	1	STB	14	-NAFD
NPRD0	2	PD0	15	-LPT-ERR
NPRD1	3	PD1	16	-NPINIT
NPRD2	4	PD2	17	-NSLIN
NPRD3	5	PD3	18	
NPRD4	6	PD4	19	
NPRD5	7	PD5	20	
NPRD6	8	PD6	21	
NPRD7	9	PD7	22	
LPT-ACK	10	ACK	23	
LPT-BUSY	11	BUSY	24	
LPT-PE	12	PE	25	
LPT-SLCT	13	SLCT		

COM





## External Connection



## BOM Difference

Location	ALC662 5.1 CH	VT1708A 5.1 CH	VT1708B 5.1 CH
Ca	20K-1-04	5.1K-1-04	5.1K-1-04
Cb	X	V	X
Cc	ALC662	VT1708AS	VT1708BS
Cd	V	X	X
Ce	X	V	V
Cf	V	X	V
Cg	X	V	X
Ci	V	X	X

02-301-662690

ALC662-GR

When you change BOM, remember change GPI to inform  
BIOS use different VerB-Table.

02-301-708621

VT1708BS

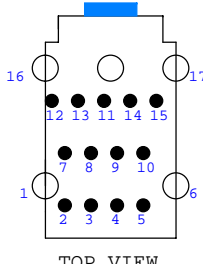
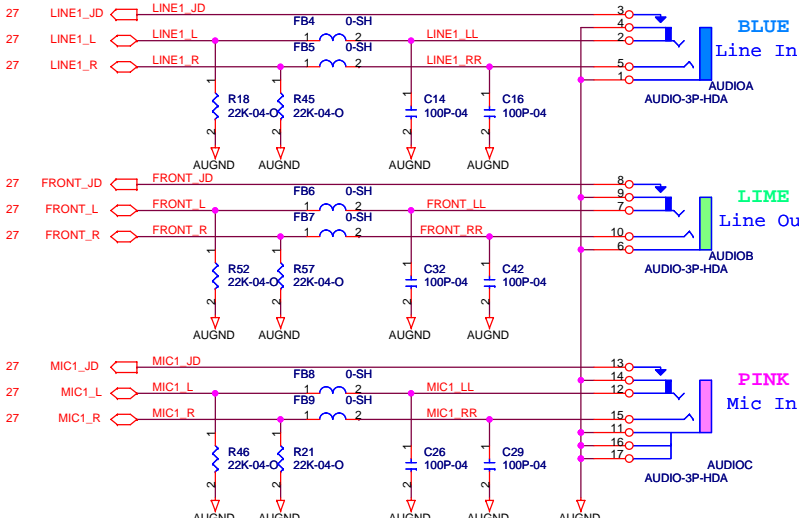
**Elitegroup Computer Systems**

**AUDIO VT1708BS(CHIP)**  
**G31T-M7**

Title: \_\_\_\_\_  
 Size Custom: \_\_\_\_\_  
 Date: Wednesday, May 28, 2008

Document Number: \_\_\_\_\_  
 Rev: **1.0**  
 Sheet 27 of 30

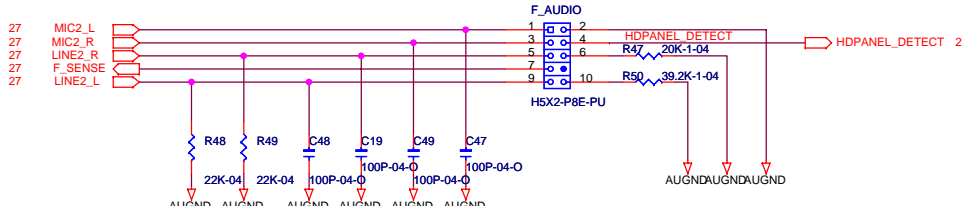
## REAR-AUDIO



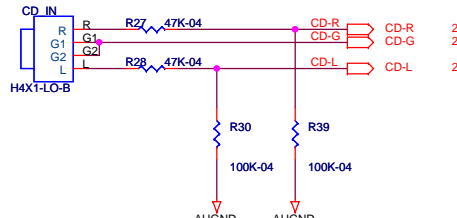
TOP VIEW



## FRONT-AUDIO



CD IN



## SPDIF-OUT

